



## ANSYS and Subsidiary Apache Receive TSMC 20 nm Phase I Certification

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PITTSBURGH, June 4, 2012 /PRNewswire/ -- [ANSYS](#) (NASDAQ: ANSS) and subsidiary Apache Design announced today that their [RedHawk™](#) and [Totem™](#) products achieved TSMC Phase I 20 nm certification. TSMC certified the tools for 20 nm design rule manuals (DRMs) and SPICE models. Early adopters are using the flows and tools while close collaboration continues between TSMC, ANSYS, Apache and designers.

(Logo: <http://photos.prnewswire.com/prnh/20110127/MM38081LOGO> )

RedHawk and Totem provide power, noise and reliability signoff for system-on-chip and mixed-signal designs at advanced technology nodes. RedHawk also addresses electromigration by delivering current direction-aware, metal topology-aware and temperature-aware checks as well as expanded capabilities to support TSMC's 20 nm electromigration rules. Totem provides full-chip, layout-based power and noise analysis for analog and mixed-signal designs.

In addition, ANSYS technologies interface with TSMC's 3D-IC flow to deliver accurate thermal profiles through iterative analysis of power maps and boundary conditions.

"Apache delivers innovative solutions that address power, noise and reliability challenges for the most advanced process nodes and emerging design technologies," said Andrew Yang, president of Apache, a subsidiary of ANSYS. "Our collaboration with TSMC enables us to provide optimized tools and methodologies for advanced-technology designers."

"With ANSYS and Apache 20 nm certification at TSMC, companies will be able to produce more robust designs and deliver next-generation products to the marketplace," said Suk Lee, TSMC senior director, design infrastructure marketing division.

The ANSYS/Apache portfolio of product offerings will be showcased at DAC exhibit booth #1813.

### [About Apache Design, Inc.](#)

Apache Design, an ANSYS subsidiary, enables simulation-driven IC and electronic systems design by providing advanced chip-level power analysis, optimization, and sign-off solutions. Apache's integrated products and methodologies advance low-power innovation and address chip-package-system power and noise challenges. Using Apache's engineering simulation software early in the design and throughout the process enables the world's top semiconductor companies to gain a competitive advantage delivering more power-efficient, high-performance, and noise immune chips. Apache's products help lower power consumption, increase operating performance, mitigate design risks, reduce system cost, and shorten time-to-market for a broad range of end-markets and applications. Learn more at: <http://www.apache-da.com/>.

### [About ANSYS, Inc.](#)

ANSYS brings clarity and insight to customers' most complex design challenges through fast, accurate and reliable engineering simulation. Our technology enables organizations — no matter their industry — to predict with confidence that their products will thrive in the real world. Customers trust our software to help ensure product integrity and drive business success through innovation. Founded in 1970, ANSYS employs more than 2,200 professionals, many of them expert in engineering fields such as finite element analysis, computational fluid dynamics, electronics and electromagnetics, and design optimization. Headquartered south of Pittsburgh, U.S.A., ANSYS has more than 65 strategic sales locations throughout the world with a network of channel partners in 40+ countries. Visit [www.ansys.com](http://www.ansys.com) for more information.

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